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PATENT 0649-0941P

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant:

Kenkichi HAYASHI

Conf.:

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Appl. No.:

10/770,501

Group:

Filed:

February 4, 2004

Examiner:

For:

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A SIGNAL PROCESSING METHOD, A SIGNAL

PROCESSOR CIRCUIT, AND IMAGING APPARATUS

LETTER SUBMITTING FORMAL DRAWINGS

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

June 8, 2004

Sir:

Attached hereto is/are five (5) sheet(s) of formal drawings which comply with the provisions of 37 C.F.R. § 1.84. The drawings should be made a part of the record of the above-identified application.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. §§1.16 or 1.17; particularly, extension of time fees.

Respectfully submitted,

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Attachment(s)

0649-0941P

DRA/lab

(Rev. 02/13/2004)